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APPLICANT: NEC CORP;

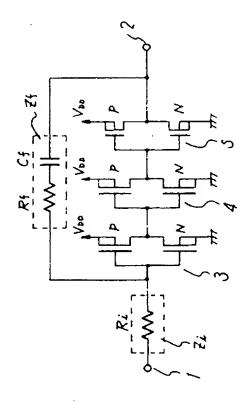
INVENTOR: NAGAOKA TOSHIAKI:

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TITLE

FEEDBACK CIRCUIT



ABSTRACT :

PURPOSE: To obtain the feedback circuit less in the output impedance and stable with less feedback voltage variation, by forming the channel length of the inverter circuit of the most post stage as the output stage shorter than other channel lengths.

CONSTITUTION: The gate electrodes of P channel MOSFET and N channel MOSFET are respectively connected commonly into the input terminals, and the drain electrodes are connected commonly each other to take it as the output terminals. The input and output terminals of the C-MOS inverter circuits 3, 4, 5 with such a constitution are mutually connected. The circuit consists of the amplifying circuit of three stage connection form, impedance element Zi between the gate electrode 1 and the input terminal 1 of the first stage inverter circuit 3, and feedback impedance element Zf provided at the feedback path between the gate electrode of the first stage inverter circuit 3 and the drain electrode of the inverter circuit 5 of the final stage. Further, the channel length of the first and second stage C-MOSFET is set longer than that of C-MOSEFET at the third stage.

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